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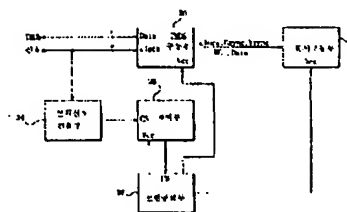
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(54) POWER-SAVING CIRCUIT FOR DIGITAL VIDEO DISPLAY DEVICE

(57) Abstract:

PURPOSE: A power-saving circuit for a digital video display device is provided to decrease a consumption power in a power-saving mode and satisfy a DTMS (Display Power Management System) standard by detecting whether a TMDS(Transmission Minimized Differential Signaling) clock signal is inputted, performing the power-saving mode according to the detected result, and stopping the driving of each unit of the device including a TMDS driving unit.



CONSTITUTION: A TMDS driving unit(20) converts a TMDS data signal inputted together with a TMDS clock signal into a horizontal/vertical synchronous signal and a digital video signal by a TMDS signal conversion method. A display driving unit(22) drives a display unit by the horizontal/vertical synchronous signal and the digital video signal inputted from the TMDS driving unit(20). A clock signal detecting unit(24) outputs a clock detecting signal with the first level when the TMDS clock signal is not inputted from the outside, and outputs a clock detecting signal with the second level when the TMDS clock signal is inputted from the outside. A control unit(26) outputs a power-saving signal with the first level when the clock detecting signal with the first level is inputted from the clock signal detecting unit (24), and outputs a power-saving signal with the second level when the clock detecting signal with the second level is inputted from the clock signal detecting unit(24). A power supply unit(28) blocks a power supply voltage supplied to each unit except for the control unit(26) when the power-saving signal with the first level, and resumes the supply of the power supply voltage when the power-saving signal with the second level is inputted from the control unit(26).

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